Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References		
Verifying the Reliability of Operating							

# Verifying the Reliability of Operating System-Level Information Flow Control in Linux

### Laurent Georget\* Mathieu JAUME† Guillaume PIOLLE‡ Frédéric TRONEL‡ Valérie VIET TRIEM TONG‡

\*Université de Rennes 1 / ‡CentraleSupélec / Inria, Rennes, France †LIP6, Sorbonne Universités, Paris, France



# An Information Flow Perspective

► Linux Operating Systems Containers of information: objects in the system able to store information originating from users, the OS environment, etc.:

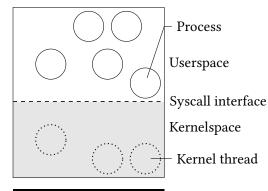
- ► files
- ► pipes
- network sockets
- message queues
- processes' memory space
- ► more?

Data **flow** from one container to another

- when reading a file
- ▶ when storing a message in a message queue
- ► etc.

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

### The information must flow

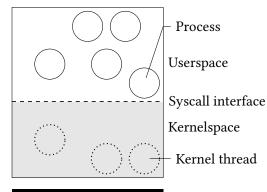


#### Hardware

- User processes are isolated
- Have no privileges
- Must use System Calls to perform privileged operations

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

# The information must flow



### User processes are isolated

- Have no privileges
- Must use System Calls to perform privileged operations

Laurent GEORGET

#### Hardware

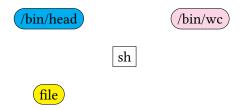
► Syscalls cause information flows

# INFORMATION FLOW TRACKERS FOR LINUX

- ► Laminar Porter et al., "Practical Fine-Grained Information Flow Control Using Laminar"
- ► **KBlare** Zimmermann, Mé, and Bidan, "An Improved Reference Flow Control Model for Policy-Based Intrusion Detection"
- ► Weir Nadkarni et al., "Practical DIFC enforcement on Android"

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

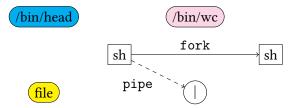
- ► Each container has a **label** identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ► Example: head file | wc



イロト イポト イラト イラト ラ

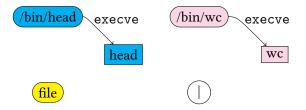
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

- ► Each container has a label identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ▶ Example: head file | wc



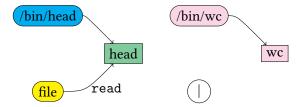
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

- ► Each container has a **label** identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ▶ Example: head file | wc



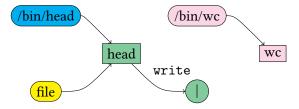
Static Analysis	Implementation and Results	Conclusion	Questions	References
	Static Analysis	Static Analysis Implementation and Results	Static Analysis Implementation and Results Conclusion	Static Analysis Implementation and Results Conclusion Questions

- ► Each container has a **label** identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ► Example: head file | wc



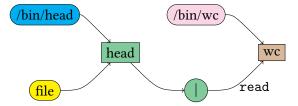
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

- ► Each container has a label identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ► Example: head file | wc



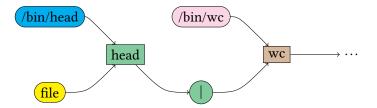
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

- ► Each container has a **label** identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ► Example: head file | wc



Static Analysis	Implementation and Results	Conclusion	Questions	References
	Static Analysis	Static Analysis Implementation and Results	Static Analysis Implementation and Results Conclusion	Static Analysis Implementation and Results Conclusion Questions

- ► Each container has a **label** identifying its initial content
- ► Each time a flow occurs, the destination label is **updated** with the source label
- ► Example: head file | wc



# EXAMPLE 1: read

fs/read\_write.c



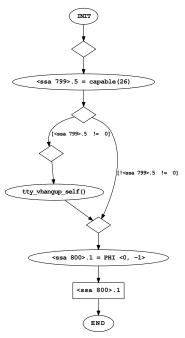
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
~					

### GRAPHS AND EXECUTION PATHS

- ► One system call = One graph
- One possible execution path = One path from INIT to END
- One instruction = One node
- One sequence or jump = One edge

Extracted directly from the **GCC compiler** Not exactly C but **GIMPLE**: intermediate representation In *Static Single Assignment* form<sup>1</sup>

<sup>1</sup>Cytron et al., "Efficiently Computing Static Single Assignment Form and the Control Dependence Graph".



```
/*
 * This routine simulates a hangup
 * on the tty, to arrange that
 * users are given clean terminals
 * at login time.
 */
SYSCALL_DEFINEO(vhangup)
ſ
  if (capable(CAP_SYS_TTY_CONFIG)) {
    tty_vhangup_self();
    return 0;
  return -EPERM;
ŀ
```

# ANATOMY OF A SYSCALL

### Syscall = Entry-point of a user process in the kernel

### ANATOMY OF A SYSCALL

Syscall = Entry-point of a user process in the kernel

- 1. Basic checks
- 2. Advanced checks / lock taking
- 3. Linux Security Modules hooks
- 4. Actual operation
- 5. Lock release
- 6. Return

# ANATOMY OF A SYSCALL

Syscall = Entry-point of a user process in the kernel

- 1. Basic checks
- 2. Advanced checks / lock taking

### 3. Linux Security Modules hooks

- 4. Actual operation
- 5. Lock release
- 6. Return

Many shortcuts exist, in case of errors.

# The Linux Security Modules Framework

LSM provides security kernel developpers with:

- Additional general-purpose security fields in kernel data structures (inodes, tasks, etc.)
- ► **Hooks** strategically placed in the syscalls code to register callbacks

10

# The Linux Security Modules Framework

LSM provides security kernel developpers with:

- Additional general-purpose security fields in kernel data structures (inodes, tasks, etc.)
- ► **Hooks** strategically placed in the syscalls code to register callbacks
- ► Expected use: LSMs store their state in the fields and use the hooks to
  - ► manage the state
  - ► authorize security-sensitive operations

・ロト・(用ト・(ヨト・(用ト・(ロト)))

Laurent GEORGET

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
Our pro	DBLEM				

Information flow trackers can only observe the execution of syscalls when called through a LSM hook.

If a syscall can generate an information flow without going through a LSM hook, that flow will be missed.

・ロト・(型ト・(型ト・(型ト・(ロト)))

Laurent GEORGET

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
Our pro	DBLEM				

Information flow trackers can only observe the execution of syscalls when called through a LSM hook.

If a syscall can generate an information flow without going through a LSM hook, that flow will be missed.

Important property to ensure a correct flow tracking

There must be a LSM hook in each execution path leading to the production of a flow in system calls.

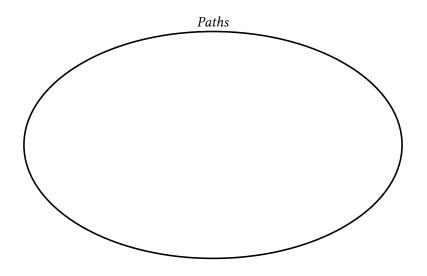
< ロ > < 同 > < 三 > < 三 > < 三 > < 回 > < ○ </p>

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
Previou	IS WORKS				

- ► Zhang, Edwards, and Jaeger, "Using CQUAL for Static Analysis of Authorization Hook Placement"
- ► Jaeger, Edwards, and Zhang, "Consistency analysis of authorization hook placement in the Linux security modules framework"
- Ganapathy, Jaeger, and Jha, "Automatic Placement of Authorization Hooks in the Linux Security Modules Framework"
- ► Muthukumaran, Jaeger, and Ganapathy, "Leveraging "choice" to automate authorization hook placement"

< ロ > < 同 > < 三 > < 三 > < 三 > < 回 > < ○ </p>

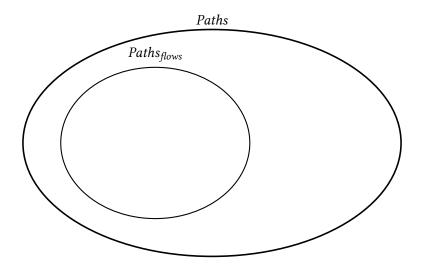
Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
					( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( )
Finding	G PROBLEMA	ATIC PATHS			



・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・

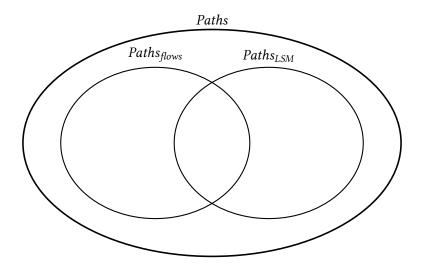
Laurent GEORGET

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References



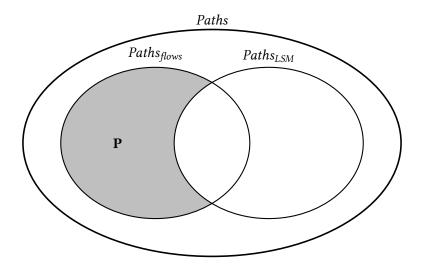
< □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References



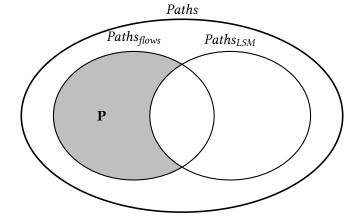
・ロト・西ト・ヨト・ヨー うへつ

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References



・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References



**P** is the set of apparently valid paths generating flows **not** covered by a LSM hooks  $\implies$  paths to analyze

Sac

비로 서로에서로에서 집에서 이다.

### Instructions causing flows and LSM hooks

LSM hooks can be automatically found in the code of system calls Instructions causing flows less so...

< ロ > < 同 > < 三 > < 三 > < 三 > < 回 > < ○ </p>

Laurent GEORGET

# Instructions causing flows and LSM hooks

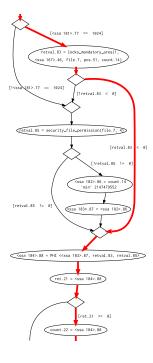
LSM hooks can be automatically found in the code of system calls Instructions causing flows less so...

Several heuristics:

- Use of locking
- End of checks
- ► Calls to architecture/hardware-dependent functions
- ► Dynamic calls through function pointers

・ロト・(用ト・(ヨト・(用ト・(ロト)))

Laurent GEORGET

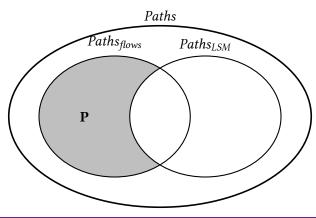


Several standard problems

Some paths are actually **impossible**: we should exclude them

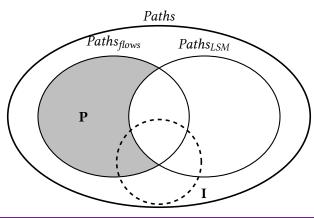
**Loops** mean there are an infinity of paths of finite length: we cannot analyze them all

<ロト < 団ト < 三ト < 三ト < 三ト < 三ト < ○への</p>



### Property (Complete mediation)

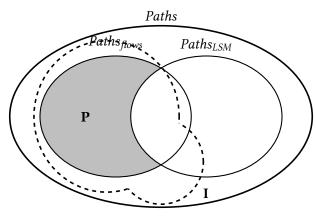
The complete mediation holds if, and only if:  $\mathbf{P} \subseteq \mathbf{I}$ , i.e. all the execution paths that perform an information flow and are not controlled by the information flow monitor since they do not contain a LSM hook are impossible according to the static analysis.



### Property (Complete mediation)

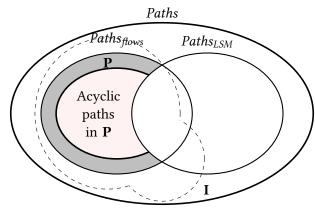
The complete mediation holds if, and only if:  $\mathbf{P} \subseteq \mathbf{I}$ , i.e. all the execution paths that perform an information flow and are not controlled by the information flow monitor since they do not contain a LSM hook are impossible according to the static analysis.

500



#### Property (Complete mediation)

The complete mediation holds if, and only if:  $\mathbf{P} \subseteq \mathbf{I}$ , i.e. all the execution paths that perform an information flow and are not controlled by the information flow monitor since they do not contain a LSM hook are impossible according to the static analysis.



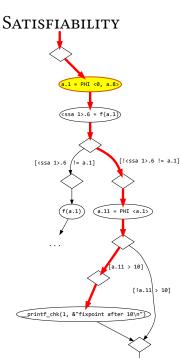
► Since **P** may be infinite, we need a way to make the analysis of the subset of acyclic paths in **P** sufficient to conclude on all paths in **P**.

# Analysis outline

General idea:

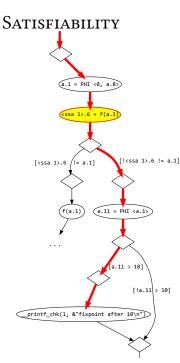
- Analyze each system call independently
- ► In each system call,
  - 1. identify nodes producing flows
  - 2. trace the paths back up until reaching either the beginning of the function or a LSM hook
  - 3. discard the paths reaching a LSM hook (paths in *Paths*<sub>LSM</sub>)
  - 4. when reaching a loop, jump to the outer-most loop header to select only acyclic paths
- ► For each analyzed path,
  - ► go through each node and edge in order
  - ► gather constraints on variables from nodes and guards on edges in a **configuration**
  - when reaching a configuration with inconsistent constraints, declare the path as impossible
  - OR when reaching the end of the path, declare it as possible

・ロト・(型ト・(型ト・(型ト・(ロト)))



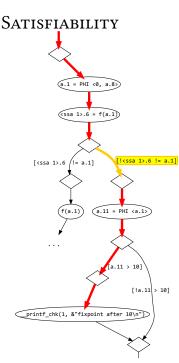
Satisfiability Current node: a.1 = PHI<0,a.8> Set of constraints: {a.1 = 0} Satisfiable: Yes

◇□▷ <□▷ < Ξ▷ < Ξ▷ < Ξ▷ < □▷ < <□</p>

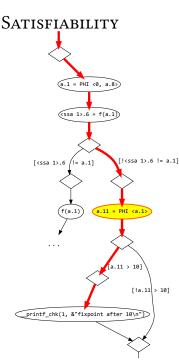


Satisfiability Current node: <ssa 1>.6 = f(a.1) Set of constraints:  $\{a.1 = 0\}$ Satisfiable: Yes

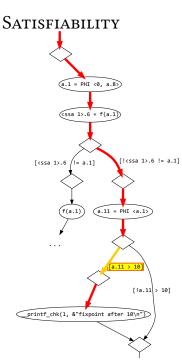
◇□▷ <□▷ < Ξ▷ < Ξ▷ < Ξ▷ < □▷ < <□</p>



Satisfiability Current edge: [!<ssa 1>.6 != a.1] Set of constraints:  $\begin{cases}
a.1 = 0, \\
<ssa 1>.6 \neq a.1 \end{cases}$ Satisfiable: Yes



 $\frac{\text{Satisfiability}}{\text{Current node: a.11}} = \text{PHI}<a.1>$ Set of constraints:  $\begin{cases} a.1 = 0, \\ < \text{ssa } 1 > .6 \neq a.1, \\ a.11 = a.1 \end{cases}$ Satisfiable: Yes

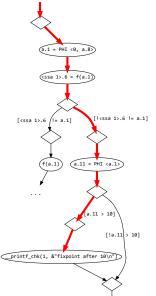


$$\label{eq:alpha} \begin{split} \underline{\text{Satisfiability}} \\ \overline{\text{Current edge: [a.11 > 10]}} \\ \text{Set of constraints:} \\ \left\{ \begin{array}{c} a.1 = 0, \\ < & \text{ssa 1>.6 \neq a.1,} \\ a.11 = a.1, \\ a.11 > 10 \end{array} \right\} \end{split}$$

Satisfiable: No  $\implies$  path impossible

・ロト < 団ト < 三ト < 三ト < 三ト < ロト</li>

## Satisfiability



#### Satisfiability

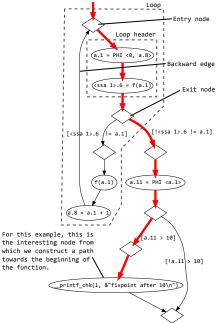
Set of constraints:

$$\left\{ egin{array}{c} a.1 = 0, \  .6 
eq a.1, \ a.11 = a.1, \ a.11 > 10 \end{array} 
ight\}$$

Satisfiable: No  $\implies$  path impossible

The satisfiability is verified by SMT-solver Yices <sup>2</sup>.

## HANDLING LOOPS



#### Dealing with loops

Loops have a special syntax and are detected by GCC

We define a **equivalence relation** on paths : two paths are equivalent if they are identical up to their cycles.

We analyze only acyclic paths (**normal form**)

When there is a loop, we remove constraints about all variables modified inside the loop.  $\implies$ The number of iterations of loops does not change the resulting configuration.

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
IMPLEM	ENTATION				

The analysis is implemented as **Kayrebt::PathExaminer2**, a GCC 4.8 plugin $^{3,4}$ .

No extraction of CFGs needed: the analysis works on GCC's CFG.

Deep insertion inside the compilation process: after the optimized phase.

Needs a previous annotation of nodes causing information flows and inlinable functions (can be done with **Kayrebt::Callgraphs**)

<sup>3</sup>Richard Matthew Stallman and the GCC developer community. Using the GNU Compiler Collection (GCC). . Tech. rep. 2013. URL:

https://gcc.gnu.org/onlinedocs/gcc-4.8.4/gcc/ (visited on 05/18/2015). <sup>4</sup>Emese Revfy. Introduce GCC plugin infrastructure. Published: Patch submitted to the kernel mailing-list. 2016. ・ロト・4日ト・4日ト・4日ト・4日・900 FormaliSE - 2017-05-27 20

### Results – Explanations

- $\checkmark$  : Everything is all right, complete mediation is ensured
- $\sim$  : We have identified some problems: some paths which should be impossible and are not
- $\times$  : We wanted to analyze the paths but there are actually no LSM hooks in the system call

・ロト・(用ト・(ヨト・(用ト・(ロト)))

References

### Results - read, write, AND THEIR KIN

Syscall	Result	Details
read	$\checkmark$	All paths in $\mathbf{P}$ are impossible
readv	$\checkmark$	All paths in <b>P</b> are impossible
preadv	$\checkmark$	All paths in <b>P</b> are impossible
pread64	$\checkmark$	All paths in <b>P</b> are impossible
write	$\checkmark$	All paths in <b>P</b> are impossible
writev	$\checkmark$	All paths in <b>P</b> are impossible
pwritev	$\checkmark$	All paths in <b>P</b> are impossible
pwrite64	$\checkmark$	All paths in <b>P</b> are impossible
sendfile	$\checkmark$	All paths in <b>P</b> are impossible
sendfile64	$\checkmark$	All paths in $\mathbf{P}$ are impossible

## Results - splice-like system calls

Syscall	Result	Details
splice	~	No hook for the pipe-to-pipe flow
		All other paths are impossible
tee	×	No LSM hook
vmsplice	$\sim$	One path is possible

・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

## Results - network-specific system calls

Syscall	Result	Details
recv	$\checkmark$	Set <b>P</b> is empty
recvmsg.	$\checkmark$	Set <b>P</b> is empty
recvmmsg	$\sim$	One path is possible
recvfrom	$\checkmark$	Set <b>P</b> is empty
send	$\checkmark$	Set <b>P</b> is empty
sendmsg.	$\checkmark$	Set <b>P</b> is empty
sendmmsg	$\sim$	One path is possible
sendto	$\checkmark$	Set <b>P</b> is empty

< □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □

## Results – processes' life

Syscall	Result	Details
fork	$\checkmark$	Set <b>P</b> is empty
vfork	$\checkmark$	Set <b>P</b> is empty
clone	$\checkmark$	Set <b>P</b> is empty
execve	$\checkmark$	Set <b>P</b> is empty
execveat	$\checkmark$	Set <b>P</b> is empty

< □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □

## $\ensuremath{\mathsf{Results}}-\ensuremath{\mathsf{System}}\xspace V$ and POSIX message queues

Syscall	Result	Details
msgrcv	$\checkmark$	All paths in <b>P</b> are impossible
msgsnd	$\checkmark$	Set <b>P</b> is empty
mq_timedreceive	×	No LSM hook
mq_timedsend	×	No LSM hook

・ロト・(型ト・(型ト・(型ト・(ロト)))

# Results - Memory-to-memory flows

Syscall	Result	Details
process_vm_readv.	$\checkmark$	Some paths possible but not con- sidered an actual flow
process_vm_writev	$\checkmark$	Some paths possible but not con- sidered an actual flow
migrate_pages	$\checkmark$	Set <b>P</b> is empty
move_pages	$\checkmark$	Set <b>P</b> is empty
shmat	$\checkmark$	Set <b>P</b> is empty
mmap_pgoff	$\checkmark$	Set <b>P</b> is empty
mmap	$\checkmark$	Set <b>P</b> is empty
ptrace	$\checkmark$	Some paths possible but not con- sidered an actual flow

< □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
Outco	ME				

Interesting results:

- confort the idea that it is possible to do information flow tracking with LSM
- highlight some holes in the design and implementation of LSM with respect to information flow tracking
- ► give a verifiable and reproducible way to analyze and improve the LSM framework

・ロト・(型ト・(型ト・(型ト・(ロト)))

#### STATIC ANALYSIS ASSISTED BY THE COMPILER

The GCC plugin interface has been opened to implement optimizations passes.

But! It is also a new way of performing static analysis! Already used in the Linux kernel<sup>5</sup>

<sup>5</sup>Emese Revfy. Introduce GCC plugin infrastructure. Published: Patch submitted to the kernel mailing-list. 2016.

FormaliSE - 2017-05-27

#### STATIC ANALYSIS ASSISTED BY THE COMPILER

The GCC plugin interface has been opened to implement optimizations passes.

But! It is also a new way of performing static analysis! Already used in the Linux kernel<sup>5</sup>

#### Benefits

- ► GCC data structures available: CFGs, points-to oracle, etc.
- ► Analysis can be done on simpler intermediate representations
- ► Ability to deal with GCCisms
- ► The code that is analyzed is not the code that is written but the code that will get executed (or at least, a closer form thereof)

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References
On-goi	NG WORK				

Cover more overt and covert channels of information flows in a correct, verifiable way.

In particular, deal with mmap-ed files and shared memories.

Deal with concurrency between flows.

・ロト < 母ト < ヨト < ヨト < ヨコ のQQ</li>

# Thank you for your attention.

Questions?

<ロト < 目 > < 目 > < 目 > < 目 > < 0 < 0</p>

- Ron Cytron et al. "Efficiently Computing Static Single Assignment Form and the Control Dependence Graph". In: ACM Transactions on Programming Languages and Systems 13.4 (Oct. 1991), pp. 451–490. URL: http://doi.acm.org/10.1145/115372.115320.
- Bruno Dutertre and Leonardo de Moura. The Yices SMT solver. Tech. rep. SRI International, 2006.
  - Vinod Ganapathy, Trent Jaeger, and Somesh Jha. "Automatic Placement of Authorization Hooks in the Linux Security Modules Framework". In: ACM Conference on Computer and Communications Security. ACM, 2005.
  - Trent Jaeger, Antony Edwards, and Xiaolan Zhang. "Consistency analysis of authorization hook placement in the Linux security modules framework". In: ACM Trans. Inf. Syst. Secur. 7.2 (2004).

・ロト・(型ト・(型ト・(型ト・(ロト)))

Divya Muthukumaran, Trent Jaeger, and Vinod Ganapathy. "Leveraging "choice" to automate authorization hook placement". In: ACM Conference on Computer and Communications Security. ACM, 2012.



Adwait Nadkarni et al. "Practical DIFC enforcement on Android". In: USENIX Security Symposium. 2016.

- Donald E. Porter et al. "Practical Fine-Grained Information Flow Control Using Laminar". In: ACM Transactions on Programming Languages and Systems 37.1 (Nov. 2014).
- Emese Revfy. Introduce GCC plugin infrastructure. Published: Patch submitted to the kernel mailing-list. 2016.
- Richard Matthew Stallman and the GCC developer community. Using the GNU Compiler Collection (GCC). Tech. rep. 2013. URL: https://gcc.gnu.org/onlinedocs/gcc-4.8.4/gcc/ (visited on 05/18/2015).

Introduction	Static Analysis	Implementation and Results	Conclusion	Questions	References

- Xiaolan Zhang, Antony Edwards, and Trent Jaeger. "Using CQUAL for Static Analysis of Authorization Hook Placement". In: USENIX Security Symposium. USENIX Association, 2002.
- Jacob Zimmermann, Ludovic Mé, and Christophe Bidan. "An Improved Reference Flow Control Model for Policy-Based Intrusion Detection". In: **Computer Security – ESORICS 2003**. Lecture Notes in Computer Science 2808. Springer Berlin Heidelberg, Oct. 13, 2003, pp. 291–308.

< ロ > < 同 > < 三 > < 三 > < 三 > < 回 > < ○ </p>

## VARIABLES

Variables are separated in 2x2 categories:

- ► Vars<sup>mem</sup> vs. Vars<sup>temp</sup>
  - ► *Vars<sup>mem</sup>*: Aliasable variables
  - ► *Vars<sup>temp</sup>*: Variables whose address is never taken
- $Vars^{ptr}$  vs.  $Vars^{\mathbb{Z}}$ 
  - ► *Vars<sup>ptr</sup>*: Pointers
  - $Vars^{\mathbb{Z}}$ : Numeric variables

The typing is enforced by the compiler.

Many variables are synthetized by the compiler itself to maintain the SSA property.

< ロ > < 同 > < 三 > < 三 > < 三 > < 回 > < ○ </p>

NODE TYPES

## Simple assignments

<ssa 183>.87 = <ssa 182>.86

Effects: Case x = y Add a constraint x = yCase p = & y Add a mapping  $p \leftrightarrow y$ 

FormaliSE - 2017-05-27

・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・
 ・

### Node types

# Assignments through pointers

Effects:

- If there is a mapping  $a.1 \leftrightarrow x$ , add a constraint x = y
- ► Otherwise, remove all constraints about variables *a*.1 may point to (GCC has a points-to oracle)

・ロト・(用ト・(ヨト・(用ト・(ロト)))

NODE TYPES

## Phi nodes

<ssa 184>.88 = PHI«ssa 183>.87, retval.83>

Found after nodes where several edges meet.

Effects:

$$x = PHI < e_1, e_2, \ldots, e_n >$$

Add a constraint  $x = e_i$  where  $e_i$  correspond to the branch taken in this path

・ロト・(用ト・(ヨト・(用ト・(ロト)))

Laurent GEORGET

Node types

# Function calls

retval.85 = security\_file\_permission(file.7, 4)

Effects:

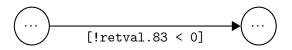
- Remove constraints on the return value
- Remove constraints on variables in Vars<sup>mem</sup>

Portions of assembly code are also represented with this node

・ロト・(用ト・(ヨト・(用ト・(ロト)))

Laurent GEORGET

### Edges



Effects:

- Add the constraint corresponding to the guard
- The operator is one of  $\{=, \neq, <, >, \ge, \le\}$

Guards on edges with the same source node are complementary

・ロト・(用ト・(ヨト・(用ト・(ロト)))

Laurent GEORGET